

## Dynamic Engineers Inc.

Website: www.DynamicEngineers.com Email: Inquiry@DynamicEngineers.com

#### **Features and Benefits**

Frequency range: 15-2100MHz Output: CML Supply voltage: 1.8V Current: 70mA Max. Frequency stability vs. temperature: ±25PPM Operating temperature: -20°C to +70°C Size: 2.5x2x1mm Package type: SMD



VCXO2520BM-LJ\_CML-223

Oscillator

Low Jitter VCXO\_Voltage Controlled Crystal

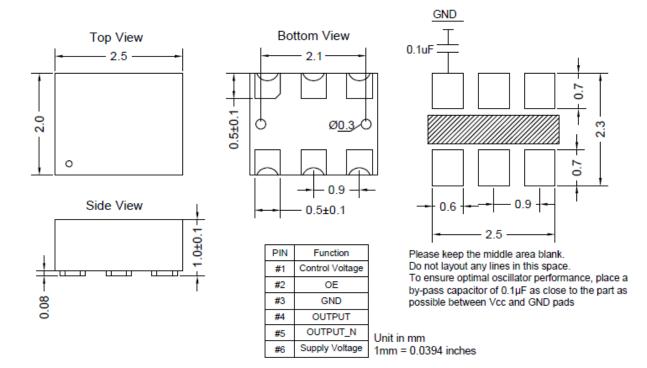
#### **Typical Applications**

**Defense Systems** Mobile Radar Station Gigabit Ethernet, SONET/SDH Server & Storage, Data Center SD/HD Video, FPGA Clock Generation

### **Mechanical Drawing & Pin Connections**







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## **Specifications**

Oscillator			Value			Unit	Note
Specification	Sym	Condition	Min.	Тур.	Max.		
Operational Frequency	f <sub>0</sub>		15		2100	MHz	
RF Output							
Output Waveform				CML			
Output Level		Output high	Vcc-0.085		Vcc	V	
		Output low	Vcc-0.6		Vcc-0.32	V	
Duty Cycle			45		55	%	
Rise & Fall Time					0.35	ns	
Startup Time					8	ms	
Tri-State		Enable	0.7 V <sub>cc</sub>			V	
(Input to Pin2)		Disable			0.3 V <sub>cc</sub>	V	
Power Supply							
Voltage	Vcc	±10%		1.8		V	
Supply Current		V <sub>cc</sub> =1.8V			70	mA	
Stand by Current		V <sub>cc</sub> =1.8V			70	mA	
Control Voltage							
Control Voltage	Vc	V <sub>cc</sub> =1.8V	0.18	0.9	1.62	V	
Pulling Range			±50		±250	ppm	
Linearity					±10	%	
Modulation Bandwidth			5		20	KHz	
Vc Input Impedance			5			Mohm	
Frequency Stability							
Versus Temperature					±25	ppm	
Phase Noise At Vcc=3.3V, 805.664MHz Frequency		1KHz		-107		dBc/Hz	
		10KHz		-117			
		100KHz		-125			
		1MHz		-135			
RMS Phase Jitter		Integrated 12KHz-20MHz	150		300	fs	
Period Jitter					50	ps	
Environmental Conditio	ns						
Operating temperature ra		-20°C to +70°C					