

Dynamic Engineers Inc.

Website: www.DynamicEngineers.com Email: Inguiry@DynamicEngineers.com

Features and Benefits

Frequency range: 15-2100MHz Output: LVDS Supply voltage: 1.8V Current: 70mA Max. Frequency stability vs. temperature: ±100PPM Operating temperature: -20°C to +70°C Size: 2.5x2x1mm Package type: SMD



VCXO2520BM-LJ_LVDS-243

Oscillator

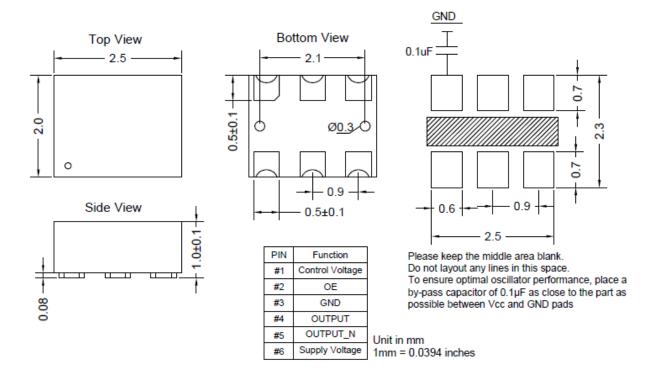
Low Jitter VCXO_Voltage Controlled Crystal

Typical Applications

Defense Systems Mobile Radar Station Gigabit Ethernet, SONET/SDH Server & Storage, Data Center SD/HD Video, FPGA Clock Generation

Mechanical Drawing & Pin Connections





Dynamic Engineers reserves the right to make changes to the company datasheet(s) along with other information contained inside; such as data tables and araphs without notification to potential customers who may have earlier revisions in their possession.



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Specifications

Oscillator Specification	Sym	Condition	Value			Unit	Note
			Min.	Тур.	Max.		
Operational	f ₀		15		2100	MHz	
Frequency	10		10		2100		
RF Output	-						1
Output Waveform				LVDS			
Output Level		Output high			1.6	V	
		Output low	0.9			V	
Duty Cycle			45		55	%	
Rise & Fall Time					0.35	ns	
Startup Time					8	ms	
Tri-State		Enable	$0.7 V_{cc}$			V	
(Input to Pin2)		Disable			$0.3 V_{cc}$	V	
Power Supply							
Voltage	Vcc	±10%		1.8		V	
Supply Current		V _{cc} =1.8V			70	mA	
Stand by Current		V _{cc} =1.8V			70	mA	
Control Voltage							
Control Voltage	Vc	V _{cc} =1.8V	0.18	0.9	1.62	V	
Pulling Range			±50		±250	ppm	
Linearity					±10	%	
Modulation Bandwidth			5		20	KHz	
Vc Input Impedance			5			Mohm	
Frequency Stability							
Versus Temperature					±100	ppm	
Phase Noise		1KHz		-106			
At V _{cc} =3.3V,		10KHz		-115		dBc/Hz	
873.515MHz		100KHz		-123			
Frequency		1MHz		-133			
RMS Phase Jitter		Integrated 12KHz-20MHz	150		300	fs	
Period Jitter					50	ps	
Environmental Conditi	ons						
Operating temperature r	ange	-20°C to +70°C					