

## Dynamic Engineers Inc.

Website: <a href="www.DynamicEngineers.com">www.DynamicEngineers.com</a></a>
Email: <a href="mailto:lnquiry@DynamicEngineers.com">lnquiry@DynamicEngineers.com</a>

#### VCXO3225BM-LJ\_HCSL-242

Low Jitter VCXO\_Voltage Controlled Crystal Oscillator

#### **Features and Benefits**

Frequency range: 15-700MHz Output waveform: HCSL Supply voltage: 2.5V Current: 80mA Max.

Frequency stability vs. temperature: ±100PPM

Operating temperature: -20°C to +70°C

Size: 3.2x2.5x1mm

Package type: Surface Mount



#### **Typical Applications**

Defense Systems
Mobile Radar Station
Gigabit Ethernet, SONET/SDH
Server & Storage, Data Center
SD/HD Video, FPGA Clock Generation

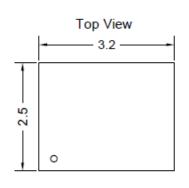
#### **Description**

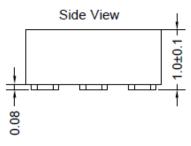
VCXO3225BM-LJ\_HCSL-242 is the high frequency and low jitter differential VCXO. It can be widely used in digital circuits.

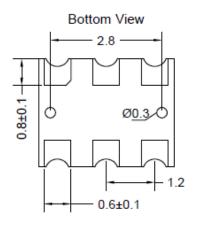
### **Mechanical Drawing & Pin Connections**

Drawing No: N

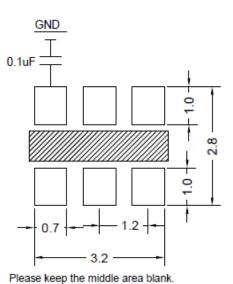
MD240085-1







| PIN | Function        |  |  |  |  |
|-----|-----------------|--|--|--|--|
| #1  | Control Voltage |  |  |  |  |
| #2  | OE              |  |  |  |  |
| #3  | GND             |  |  |  |  |
| #4  | OUTPUT          |  |  |  |  |
| #5  | OUTPUT_N        |  |  |  |  |
| #6  | Supply Voltage  |  |  |  |  |



Do not layout any lines in this space. To ensure optimal oscillator performance, place a by-pass capacitor of  $0.1\mu F$  as close to the part as possible between Vcc and GND pads

Unit in mm 1mm = 0.0394 inches



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# **Specifications**

| Oscillator<br>Specification                                      | Sym   | Condition              | Value               |      |                     | Unit   | Note |
|--|-------|------------------------|---------------------|------|---------------------|--------|------|
|  |       |                        | Min.                | Тур. | Max.                |        |      |
| Operational Frequency  | $f_0$ |                        | 15                  |      | 700                 | MHz    |      |
| RF Output  |       |                        |                     |      |                     |        |      |
| Output Waveform  |       |                        |                     | HCSL |                     |        |      |
| Output Level   |       | Output high            | 0.66                |      | 1.15                | V      |      |
|  |       | Output low             | 0                   |      | 0.15                | V      |      |
| Duty Cycle   |       |                        | 45                  |      | 55                  | %      |      |
| Rise & Fall Time   |       |                        |                     |      | 0.35                | ns     |      |
| Startup Time   |       |                        |                     |      | 8                   | ms     |      |
| Tri-State  |       | Enable                 | 0.7 V <sub>cc</sub> |      |                     | V      |      |
| (Input to Pin2)  |       | Disable                |                     |      | 0.3 V <sub>cc</sub> | V      |      |
| Power Supply   |       |                        |                     |      |                     |        |      |
| Voltage  | Vcc   | ±10%                   |                     | 2.5  |                     | V      |      |
| Supply Current   |       | V <sub>cc</sub> =2.5V  |                     |      | 80                  | mA     |      |
| Stand by Current   |       | V <sub>cc</sub> =2.5V  |                     |      | 80                  | mA     |      |
| Control Voltage  |       |                        |                     |      |                     |        |      |
| Control Voltage  |       | V <sub>cc</sub> =2.5V  | 0.25                | 1.25 | 2.25                | V      |      |
| Pulling Range  |       |                        | ±50                 |      | ±250                | ppm    |      |
| Linearity  |       |                        |                     |      | ±10                 | %      |      |
| Modulation Bandwidth   |       |                        | 5                   |      | 20                  | KHz    |      |
| VC Input Impedance   |       |                        | 5                   |      |                     | Mohm   |      |
| Frequency Stability  |       |                        |                     |      |                     |        |      |
| Versus Temperature   |       |                        |                     |      | ±100                | ppm    |      |
| Phase Noise<br>At V <sub>cc</sub> =3.3V,<br>805.664MHz Frequency |       | 1KHz                   |                     | -107 |                     | dBa/Uz |      |
|  |       | 10KHz                  |                     | -117 |                     |        |      |
|  |       | 100KHz                 |                     | -125 |                     | dBc/Hz |      |
|  |       | 1MHz                   |                     | -135 |                     | 1      |      |
| RMS Phase Jitter   |       | Integrated 12KHz-20MHz | 150                 |      | 300                 | fs     |      |
| Period Jitter  |       |                        |                     |      | 50                  | ps     |      |
| <b>Environmental Conditio</b>                                    | ns    |                        |                     |      |                     |        |      |
| Operating temperature ra   | nge   | -20°C to +70°C         |                     |      |                     |        |      |