

## Dynamic Engineers Inc.

Website: <a href="www.DynamicEngineers.com">www.DynamicEngineers.com</a></a>
Email: <a href="mailto:lnquiry@DynamicEngineers.com">lnquiry@DynamicEngineers.com</a>

#### VCXO3225BM-LJ\_LVDS-111

Low Jitter VCXO\_Voltage Controlled Crystal Oscillator

## **Features and Benefits**

Frequency range: 15-2100MHz Output waveform: LVDS Supply voltage: 1.8V Current: 70mA Max.

Frequency stability vs. temperature: ±20PPM Operating temperature: -10°C to +60°C

Size: 3.2x2.5x1mm

Package type: Surface Mount



## **Typical Applications**

Defense Systems
Mobile Radar Station
Gigabit Ethernet, SONET/SDH
Server & Storage, Data Center
SD/HD Video, FPGA Clock Generation

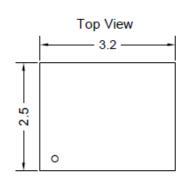
#### **Description**

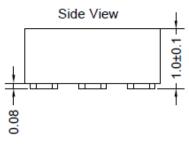
VCXO3225BM-LJ\_LVDS-111 is the high frequency and low jitter differential VCXO. It can be widely used in digital circuits.

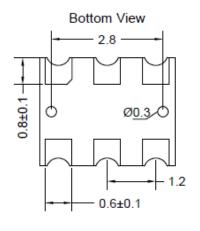
## **Mechanical Drawing & Pin Connections**

Drawing No: MD24

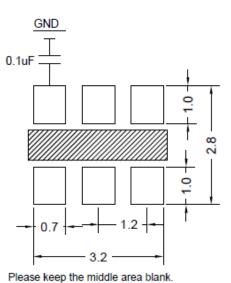
MD240085-1







| PIN | Function        |  |  |  |  |
|-----|-----------------|--|--|--|--|
| #1  | Control Voltage |  |  |  |  |
| #2  | OE              |  |  |  |  |
| #3  | GND             |  |  |  |  |
| #4  | OUTPUT          |  |  |  |  |
| #5  | OUTPUT_N        |  |  |  |  |
| #6  | Supply Voltage  |  |  |  |  |



Do not layout any lines in this space. To ensure optimal oscillator performance, place a by-pass capacitor of 0.1µF as close to the part as possible between Vcc and GND pads

Unit in mm 1mm = 0.0394 inches



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# **Specifications**

| Oscillator<br>Specification                                 | Sym            | Condition              | Value        |      |         | Unit     | Note |
|---|----------------|------------------------|--------------|------|---------|----------|------|
|   |                |                        | Min.         | Тур. | Max.    |          |      |
| Operational Frequency                                       | f <sub>0</sub> |                        | 15           |      | 2100    | MHz      |      |
| RF Output   |                |                        |              |      |         |          |      |
| Output Waveform   |                |                        |              | LVDS |         |          |      |
| Output Level  |                | Output high            |              |      | 1.6     | V        |      |
|   |                | Output low             | 0.9          |      |         | V        |      |
| Duty Cycle  |                |                        | 45           |      | 55      | %        |      |
| Rise & Fall Time  |                |                        |              |      | 0.35    | ns       |      |
| Startup Time  |                |                        |              |      | 8       | ms       |      |
| Tri-State   |                | Enable                 | $0.7~V_{cc}$ |      |         | V        |      |
| (Input to Pin2)   |                | Disable                |              |      | 0.3 Vcc | V        |      |
| Power Supply  |                |                        |              |      |         |          |      |
| Voltage   | $V_{cc}$       | ±10%                   |              | 1.8  |         | V        |      |
| Supply Current  |                | V <sub>cc</sub> =1.8V  |              |      | 70      | mA       |      |
| Stand by Current  |                | V <sub>cc</sub> =1.8V  |              |      | 70      | mA       |      |
| Control Voltage   |                |                        |              |      |         |          |      |
| Control Voltage   |                | V <sub>cc</sub> =1.8V  | 0.18         | 0.9  | 1.62    | V        |      |
| Pulling Range   |                |                        | ±50          |      | ±250    | ppm      |      |
| Linearity   |                |                        |              |      | ±10     | %        |      |
| Modulation Bandwidth  |                |                        | 5            |      | 20      | KHz      |      |
| VC Input Impedance  |                |                        | 5            |      |         | Mohm     |      |
| Frequency Stability   |                |                        |              |      |         |          |      |
| Versus Temperature  |                |                        |              |      | ±20     | ppm      |      |
| Phase Noise At $V_{\infty}$ =3.3 $V$ , 873.515MHz Frequency |                | 1KHz                   |              | -106 |         | - dBc/Hz |      |
|   |                | 10KHz                  |              | -115 |         |          |      |
|   |                | 100KHz                 |              | -123 |         | ubc/112  |      |
|   |                | 1MHz                   |              | -133 |         |          |      |
| RMS Phase Jitter  |                | Integrated 12KHz-20MHz | 150          |      | 300     | fs       |      |
| Period Jitter   |                |                        |              |      | 50      | ps       |      |
| <b>Environmental Condition</b>                              | ns             |                        |              |      |         |          |      |
| Operating temperature ra                                    | nge            | -10°C to +60°C         |              |      |         |          |      |