

Dynamic Engineers Inc.

Website: www.DynamicEngineers.com Email: http://www.DynamicEngineers.com VCXO3225BM-LJ_LVDS-231 Low Jitter VCXO_Voltage Controlled Crystal Oscillator

Features and Benefits

Frequency range: 15-2100MHz Output waveform: LVDS Supply voltage: 1.8V Current: 70mA Max. Frequency stability vs. temperature: ±50PPM Operating temperature: -20°C to +70°C Size: 3.2x2.5x1mm Package type: Surface Mount



Typical Applications

Defense Systems Mobile Radar Station Gigabit Ethernet, SONET/SDH Server & Storage, Data Center SD/HD Video, FPGA Clock Generation

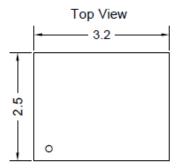
Description

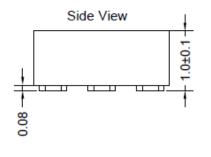
VCXO3225BM-LJ_LVDS-231 is the high frequency and low jitter differential VCXO. It can be widely used in digital circuits.

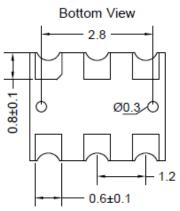
Mechanical Drawing & Pin Connections

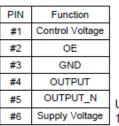


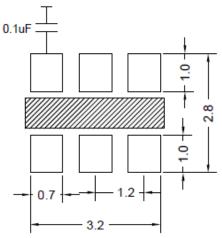
GND











Please keep the middle area blank. Do not layout any lines in this space. To ensure optimal oscillator performance, place a by-pass capacitor of $0.1\mu F$ as close to the part as possible between Vcc and GND pads

Unit in mm I^e 1mm = 0.0394 inches

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Rev. 1

Dynamic Engineers reserves the right to make changes to the company datasheet(s) along with other information contained inside, such as data tables and araphs without notification to potential customers who may have earlier revisions in their possession.



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Specifications

| Oscillator Specification | Sym | Condition | Value | | | Unit | Note |
|--|----------------|------------------------|--------------|------|---------------------|---------|------|
| | | | Min. | Тур. | Max. | | |
| Operational Frequency | f ₀ | | 15 | | 2100 | MHz | |
| RF Output | | | | | | | |
| Output Waveform | | | | LVDS | | | |
| Output Level | | Output high | | | 1.6 | V | |
| | | Output low | 0.9 | | | V | |
| Duty Cycle | | | 45 | | 55 | % | |
| Rise & Fall Time | | | | | 0.35 | ns | |
| Startup Time | | | | | 8 | ms | |
| Tri-State | | Enable | $0.7 V_{cc}$ | | | V | |
| (Input to Pin2) | | Disable | | | 0.3 V _{cc} | V | |
| Power Supply | | | | | | | |
| Voltage | Vcc | ±10% | | 1.8 | | V | |
| Supply Current | | V _{cc} =1.8V | | | 70 | mA | |
| Stand by Current | | V _{cc} =1.8V | | | 70 | mA | |
| Control Voltage | | | | | | | |
| Control Voltage | | V _{cc} =1.8V | 0.18 | 0.9 | 1.62 | V | |
| Pulling Range | | | ±50 | | ±250 | ppm | |
| Linearity | | | | | ±10 | % | |
| Modulation Bandwidth | | | 5 | | 20 | KHz | |
| VC Input Impedance | | | 5 | | | Mohm | |
| Frequency Stability | | | | | | | |
| Versus Temperature | | | | | ±50 | ppm | |
| Phase Noise At V∞=3.3V, 873.515MHz Frequency | | 1KHz | | -106 | | dBc/Hz | |
| | | 10KHz | | -115 | | | |
| | | 100KHz | | -123 | | UDC/112 | |
| | | 1MHz | | -133 | | | |
| RMS Phase Jitter | | Integrated 12KHz-20MHz | 150 | | 300 | fs | |
| Period Jitter | | | | | 50 | ps | |
| Environmental Conditio | ns | | | | | | |
| Operating temperature ra | nge | -20°C to +70°C | | | | | |